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Design of PI and Fuzzy Controllers for Dynamic Voltage Restorer (DVR)

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Abstract

Power quality is one of the major concerns in the present era. It has become important, especially, with the introduction of sophisticated devices, whose performance is very sensitive to the quality of power supply. To solve this problem, custom power devices are used. One of those devices is the Dynamic Voltage Restorer (DVR), which is the most efficient and effective modern custom power device used in power distribution networks. Its appeal includes lower cost, smaller size, and its fast dynamic response to the disturbance. This paper presents modelling, analysis and simulation of a Dynamic Voltage Restorer (DVR) using MATLAB for both PI and FUZZY CONTROLLERS.

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Keywords: Dynamic Voltage Restorer, Custom Power Devices, Dynamic Power Quality, PI, Control Strategy.

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1. Introduction

Nowadays, modern industrial applications are mostly based on electronic devices such as programmable logic controllers and electronic drives. The electronic devices are very sensitive to disturbances and become less tolerant to power quality problems such as voltage sags, swells and harmonics. Voltage dips are considered to be one of the most severe disturbances to the industrial equipments. Voltage support at a load can be achieved by reactive power injection at the load point of common coupling. The common method for this is to install mechanically switched shunt capacitors in the primary terminal of the distribution transformer. The disadvantage is that, high speed transients cannot be compensated. Another power electronic solution to the voltage regulation is the use of a custom power device. DVRs are a class of custom power devices for providing reliable distribution power quality.

2. Solutions to Power Quality Problems

There are two approaches to the mitigation of power quality problems. The solution to the power quality can be done from customer side or from utility side. First approach is called load conditioning, which ensures that the equipment is less sensitive to power disturbances and allowing the operation even under significant voltage distortion. The other solution is to install line conditioning systems that suppress or counteracts the power system disturbances. Currently they are based on PWM converters and are connected to low and medium voltage distribution system in shunt or in series. Some of the effective and economic measures identified are Lightning and Surge Arresters, Thyristor Based Static Switches, Energy Storage Systems. Though there are many different methods to mitigate voltage sags and swells, but the use of a custom Power device is considered to be the most efficient method. There are many types of Custom Power devices. Some of these devices include: Active Power Filters (APF), Battery Energy Storage Systems (BESS), Distribution Static Synchronous Compensators (DSTATCOM), Distribution Series Capacitors (DSC), Dynamic Voltage Restorer (DVR), Surge Arresters (SA), Superconducting Magnetic Energy Systems (SMES), Static Electronic Tap Changers (SETC), Solid-State Transfer Switches (SSTS), Solid State Fault Current Limiter (SSFCL), Static VAR Compensator (SVC), Thyristor Switched Capacitors (TSC) and Uninterruptible Power Supplies (UPS). Among these, the DVR is an effective custom power device which is based on the VSC principle that can deal with voltage sags and swells.

3. Dynamic Voltage Restorer (DVR)

Dynamic Voltage Restorer (DVR), is the most efficient and effective modern custom power device used in power distribution networks. DVR is a recently proposed series connected solid state device that injects voltage into the system in order to regulate the load side voltage. It is normally installed in a distribution system between the supply and the critical load feeder at the Point of Common Coupling (PCC). Other than voltage sags and swells compensation, DVR has other features like: line voltage harmonics compensation and reduction of transients in voltage and fault current limitations. The structure of the DVR is shown in fig.1. An equivalent circuit diagram of the DVR and the principle of series injection for sag/swell compensation is depicted in fig. 2. The load voltage is given by

$$V_L = V_S + V_{inj} \dots\dots\dots (1)$$

where V_S is the supply voltage and V_{inj} is the voltage injected by the mitigation device.

Under nominal voltage conditions, the load power on each phase is given by

$$S_L = V_L I_L^* = P_L + jQ_L \dots\dots\dots (2)$$

where I_L is the load current, P_L and Q_L are the real and reactive power taken by the load respectively.

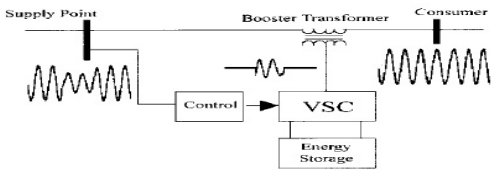


Fig. 1 Structure of DVR

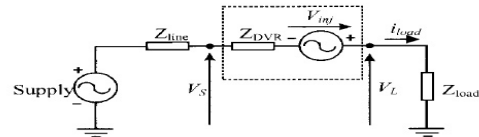


Fig. 2 Equivalent circuit of DVR

When the compensating device is active and restores the voltages back to normal, then

$$S_L = P_L - jQ_L = (P_{Sag} - jQ_{Sag}) + (P_{inj} - jQ_{inj}) \quad \dots\dots\dots (3)$$

where the sag subscript refers to the sagged supply quantities, the inject subscript refers to quantities injected by the compensator device (DVR).

4. Control Scheme

The control scheme of DVR is exhibited in Fig.3. In this, V_a, V_b, V_c are the three-phase voltages, V_{RMS} is the Root Mean Square (RMS) voltage. Finally, V_{abc}^* is the three-phase voltage desired at the converter output. The two controllers analyzed in this paper are shown in fig4 and fig.6, which employs Proportional-Integral (PI) controller and Fuzzy Proportional-Integral (FPI) controller.

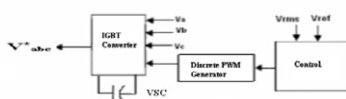


Fig. 3 Control Scheme of DVR

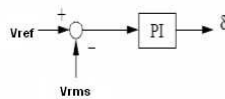


Fig. 4 PI Controller

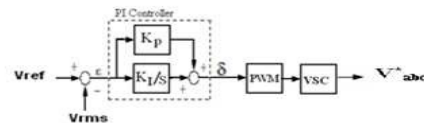


Fig. 5 PI Controller with PWM and VSC

4.1. PI Controller

The PI controller input is an error signal obtained from the reference voltage and the RMS value of the terminal voltage measured. Such error is processed by a PI controller and the output is the angle which is provided to the PWM signal generator. The PWM generator then generates the pulse signals to the IGBT gates of Voltage Source Converter (VSC). The disadvantage of PI controller is its inability to react to abrupt changes in the error signal, ϵ , because it is only capable of determining the instantaneous value of the error signal without considering the change of the rise and fall of the error, which in mathematical terms is the derivative of the error signal, denoted as $\Delta\epsilon$.

4.2. FUZZY PI (FPI) Controller

To solve this problem and to improve the behaviour of DVR, Fuzzy PI control is proposed. The determination of the output control signal, is done in an inference engine with a rule base having if-then rules in the form of IF ϵ AND $\Delta\epsilon$, THEN K_P AND K_I . With the rule base, the values of the constants K_P and K_I are changed according to the value of the error signal, ϵ , and the rate-of-error, $\Delta\epsilon$. The Fuzzy Logic Control (FLC) is based on Mamdani’s system.

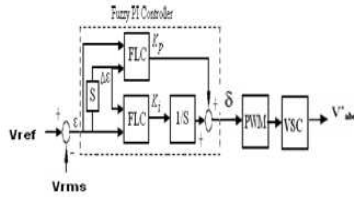


Fig. 6 Fuzzy PI Controller

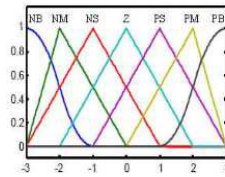


Fig.7 Membership function curves of the inputs ϵ and $\Delta\epsilon$

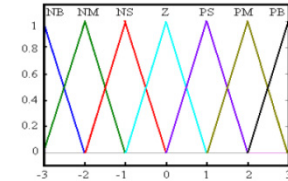


Fig.8 Membership function curves of the output K_p and K_i

The fuzzy controller has two inputs: (i) the difference between the rms voltage and the reference voltage (ii) the derivation of the error. The fuzzy logic controller consists of three stages: the fuzzification, rule execution, and defuzzification. All the variables i.e., fuzzy subsets for the inputs ϵ and $\Delta\epsilon$ are defined as (NB, NM, NS, Z, PS, PM, PB). Taking into account of the coverage, sensitivity, robustness of universe, the fuzzy subsets of the membership functions use “Z”-shaped membership function in the left, triangular membership function in the middle and “S”-shaped membership function curve in the right. The membership functions and initial universes of the inputs are illustrated in fig. 7. For the output variables K_p and K_i , the fuzzy subsets of the membership functions have a triangular shape only as it is illustrated in Figure 8. Tables 1 and 2 illustrate the fuzzy control rules for the output variables K_p and K_i .

Table 1. Fuzzy Control Rules for K_p

$e/\Delta e$	NB	NM	NS	Z	PS	PM	PB
NB	PB	PB	PM	PM	PS	PS	Z
NM	PB	PB	PM	PM	PS	Z	NS
NS	PM	PM	PM	PS	Z	NS	NM
Z	PM	PS	PS	Z	NS	NM	NM
PS	PS	PS	Z	NS	NS	NM	NM
PM	PS	Z	NS	NM	NM	NM	NB
PB	Z	NS	NS	NM	M	NB	NB

Table 2. Fuzzy Control Rules for K_i

$e/\Delta e$	NB	NM	NS	Z	PS	PM	PB
NB	NB	NB	NB	NM	NM	NS	Z
NM	NB	NB	NM	NM	NS	Z	PS
NS	NM	NM	NS	NS	Z	PS	PS
Z	NM	NS	NS	Z	PS	PS	PM
PS	NS	NS	Z	PS	PS	PM	PM
PM	NS	Z	PS	PM	PM	PB	PB
PB	Z	NS	PS	NM	PB	PB	PB

5. Simulation Results

The system shown in fig.1 with system parameters given in table-3 has been modelled by MATLAB/SIMULINK to study the performance of DVR in enhancing power quality. The results of simulation are shown separately in each case. Both sag and swell are generated from source. The simulation results are shown in the following figures, where the very effective voltage regulation provided by the DVR can be clearly appreciated in all cases.

5.1. With PI Controller

Sag and swell of 20% magnitude is generated at source side in one of the phases (sag from $t = 100$ ms to 200 ms and swell from $t = 200$ ms to 300 ms) which is shown in fig.9 (a). The PI Controller based DVR is able to mitigate voltage sag and swell and is providing voltage regulation with load side THD of 8.89 % (Fig. 9(b), (c) and (d))

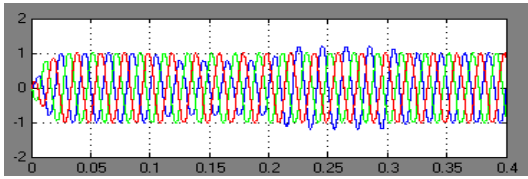


Fig.9 (a) Source Voltage with 20% voltage sag and swell in phase A

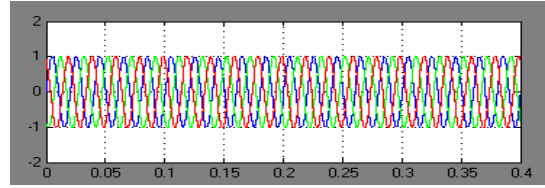


Fig.9 (b) Load Voltage after mitigating voltage sag and swell with PI Controller

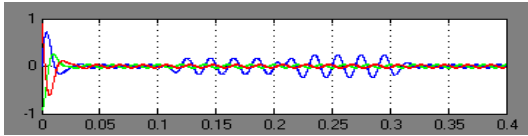


Fig.9(c) DVR injected Voltage with PI Controller

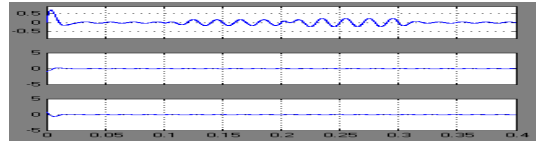


Fig.9 (d) Three phases of DVR injected Voltage with PI Controller

5.2. With Fuzzy PI Controller

20% sag (from $t = 100$ ms to 200 ms) and 20% swell (from $t = 200$ ms to 300 ms) are generated at source side in phase A. The Fuzzy PI Controller based DVR is able to mitigate voltage sag and swell and is providing voltage regulation with load side THD of 4.45% where as the THD with the PI Controller alone was 8.89%. With the Fuzzy PI Controller the THD is reduced to considerable amount and is providing better voltage regulation (Fig. 10(b), (c) and (d)). Table4 gives the comparison of PI Controller with FPI controller.

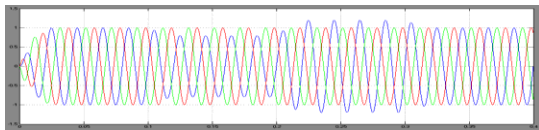


Fig.10 (a) Source Voltage with 20% voltage sag and swell in phase A

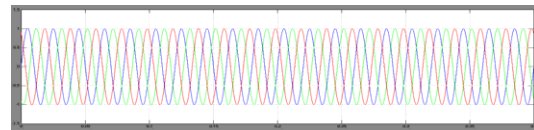


Fig.10 (b) Load Voltage after mitigating voltage sag and swell with Fuzzy PI Controller

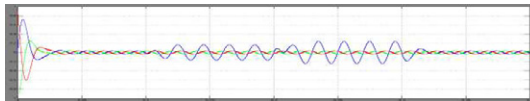


Fig.10(c) DVR injected Voltage with Fuzzy PI Controller

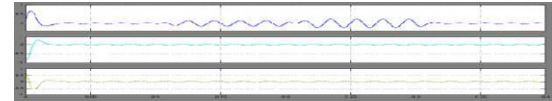


Fig. 10(d) Three phases of DVR injected Voltage with Fuzzy PI Controller

Table 3. System Parameters

Voltage: 240V	Filter Inductance: 2e-3H
Frequency: $2\pi \cdot 50$ radians/sec.	Filter Capacitance: 300e-6F
Phase: $-2\pi/3$ radians	Base Voltage: 240V
PWM carrier Frequency: 2000Hz	R Load: 40 Ohms
PWM sampling time: 2e-3sec.	L Load: $6.41/2\pi \cdot \%0$ H
Linear Transformer: 100MVA	
Primary Winding: 960V, 0.002 Ohm, 0.08H	Secondary Winding: 480V, 0.002 Ohm, 0.08H

Table 4. Comparison of PI Controller with FPI Controller

Factor	With PI DVR	With FPI DVR
Sag 20% t(sec.)= 0.1-0.2	Mitigated	Mitigated
Swell 20% t(sec.)= 0.2-0.3	Mitigated	Mitigated
Multi mode oscillations	Observed	Not observed
Source side THD	21.73%	12.07%
Load side THD	8.89%	4.45%

6. Conclusion

The paper has presented the power quality problems such as voltage dips and swells. Compensation techniques of custom power electronic devices such as DVR were presented. A PWM-based control scheme was implemented. As opposed to fundamental frequency switching schemes already available in the MATLAB/ SIMULINK, this PWM control scheme only requires voltage measurements. This characteristic makes it ideally suitable for low-voltage custom power applications. PI and FUZZY LOGIC Controllers are developed. The results show that Fuzzy PI Controller is giving better performance results than compared to PI Controllers since the load THD with FPI Controller is found to be 4.45% which is less 5%.(as per IEEE norms)

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